		lication Fe					
Application Number:	10	10083533					
Filing Date:	27-	27-Feb-2002					
Title of Invention:		SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE INCLUDING A SEMICONDUCTOR DEVICE HAVING A STABLE THRESHOLD CHARACTERISTIC					
First Named Inventor/Applicant Name:	Hir	Hiroshi Hashimoto					
Filer:	Mid	Michael J. Caridi/Gina Schwartz					
Attorney Docket Number:	020	020244					
Filed as Large Entity							
Utility under 35 USC 111(a) Filing Fees	;						
Description		Fee Code	Quantity	Amount	Sub-Total in USD(\$)		
Basic Filing:					1		
Pages:							
Claims:							
Miscellaneous-Filing:							
Petition:							
Patent-Appeals-and-Interference:							
Patent-Appeals-and-Interference: Post-Allowance-and-Post-Issuance:							

Publ. Fee- early, voluntary, or normal

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Extension-of-Time:				
Miscellaneous:				
Printed copy of patent - no color	8001	2	3	6
	Tot	1746		